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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE TC 2600 MAILROOM

In re Application of:
Grimwood, et al.

Art Unit: 2731

Serial No. 09/074,036

Examiner: E. Bayard

Filed: 05/06/98

Docket No.: TER-005

For: APPARATUS AND METHOD FOR SYNCHRONIZING AN SCDMA UPSTREAM OR ANY
OTHER TYPE UPSTREAM TO AN MCNS DOWNSTREAM OR ANY OTHER TYPE
DOWNSTREAM WITH A DIFFERENT CLOCK RATE THAN THE UPSTREAM

Honorable Commissioner
of Patents and Trademarks
Washington, D.C. 20231

Morgan Hill, California
January 5, 2001

AMENDMENT

Dear Sir:

In response to the Final Rejection mailed 10/5/00, please amend the above
identified case as follows.

IN THE CLAIMS / / / /

Please cancel claims 6 -17 and 29.

Please amend claim 3 as follows to correct an error in the recitation of
cooperation between elements.

3. (Twice Amended) A digital data transceiver comprising:

a master clock for generating a master downstream clock signal at a clock
output having a frequency which is an integer multiple of a downstream symbol
or chip rate, F_{ds} ;

a downstream modulator implementing any form of TDMA or CDMA